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ISL9R8120P2, ISL9R8120S3S

Features

- Stealth Recovery $t_{rr} = 300$ ns (@ $I_F = 8$ A)
- Max Forward Voltage, $V_F = 3.3$ V (@ $T_C = 25^\circ\text{C}$)
- 1200 V Reverse Voltage and High Reliability
- Avalanche Energy Rated
- RoHS compliant

Applications

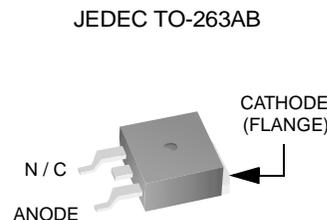
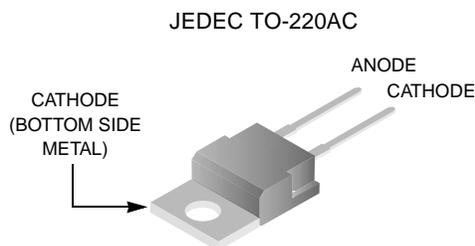
- Switch Mode Power Supplies
- Hard Switched PFC Boost Diode
- UPS Free Wheeling Diode
- Motor Drive FWD
- SMPS FWD
- Snubber Diode

8 A, 1200 V, STEALTH™ Diode

The ISL9R8120P2, ISL9R8120S3S is a STEALTH™ diode optimized for low loss performance in high frequency hard switched applications. The STEALTH™ family exhibits low reverse recovery current ($I_{RM(REC)}$) and exceptionally soft recovery under typical operating conditions. This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low $I_{RM(REC)}$ and short t_a phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the STEALTH™ diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

Package

Symbol



Device Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Unit
V_{RRM}	Repetitive Peak Reverse Voltage	1200	V
V_{RWM}	Working Peak Reverse Voltage	1200	V
V_R	DC Blocking Voltage	1200	V
$I_{F(AV)}$	Average Rectified Forward Current ($T_C = 105^\circ\text{C}$)	8	A
I_{FRM}	Repetitive Peak Surge Current (20 kHz Square Wave)	16	A
I_{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60 Hz)	100	A
P_D	Power Dissipation	71	W
E_{AVL}	Avalanche Energy (1 A, 40 mH)	20	mJ
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_L	Maximum Temperature for Soldering		$^\circ\text{C}$
T_{PKG}	Leads at 0.063 in (1.6 mm) from Case for 10 s Package Body for 10s, See Application Note AN-7528	300 260	$^\circ\text{C}$ $^\circ\text{C}$

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

ISL9R8120P2, ISL9R8120S3S 8 A, 1200 V, STEALTH™ Diode

Package Marking and Ordering Information

Device Marking	Device	Package	Tape Width	Quantity
R8120P2	ISL9R8120P2	TO-220AC	N/A	50
R8120S3S	ISL9R8120S3S	TO-263AB	24mm	800

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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Off State Characteristics

I_R	Instantaneous Reverse Current	$V_R = 1200\text{ V}$	$T_C = 25^\circ\text{C}$	-	-	100	μA
			$T_C = 125^\circ\text{C}$	-	-	1.0	mA

On State Characteristics

V_F	Instantaneous Forward Voltage	$I_F = 8\text{ A}$	$T_C = 25^\circ\text{C}$	-	2.8	3.3	V
			$T_C = 125^\circ\text{C}$	-	2.7	3.1	V

Dynamic Characteristics

C_J	Junction Capacitance	$V_R = 10\text{ V}, I_F = 0\text{ A}$	-	30	-	pF
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Switching Characteristics

t_{rr}	Reverse Recovery Time	$I_F = 1\text{ A}, di_F/dt = 100\text{ A}/\mu\text{s}, V_R = 30\text{ V}$	-	25	32	ns
		$I_F = 8\text{ A}, di_F/dt = 100\text{ A}/\mu\text{s}, V_R = 30\text{ V}$	-	35	44	ns
t_{rr}	Reverse Recovery Time	$I_F = 8\text{ A},$ $di_F/dt = 200\text{ A}/\mu\text{s},$ $V_R = 780\text{ V}, T_C = 25^\circ\text{C}$	-	300	-	ns
I_{rr}	Reverse Recovery Current		-	4.3	-	A
Q_{rr}	Reverse Recovered Charge		-	525	-	nC
t_{rr}	Reverse Recovery Time		$I_F = 8\text{ A},$ $di_F/dt = 200\text{ A}/\mu\text{s},$ $V_R = 780\text{ V},$ $T_C = 125^\circ\text{C}$	-	375	-
S	Softness Factor (t_b/t_a)	$I_F = 8\text{ A},$ $di_F/dt = 1000\text{ A}/\mu\text{s},$ $V_R = 780\text{ V},$ $T_C = 125^\circ\text{C}$	-	9	-	-
I_{rr}	Reverse Recovery Current		-	5.5	-	A
Q_{rr}	Reverse Recovered Charge		-	1.1	-	μC
t_{rr}	Reverse Recovery Time		-	200	-	ns
S	Softness Factor (t_b/t_a)	$I_F = 8\text{ A},$ $di_F/dt = 1000\text{ A}/\mu\text{s},$ $V_R = 780\text{ V},$ $T_C = 125^\circ\text{C}$	-	5.5	-	-
I_{rr}	Reverse Recovery Current		-	11	-	A
Q_{rr}	Reverse Recovered Charge		-	1.2	-	μC
di_M/dt	Maximum di/dt during t_b		-	310	-	$\text{A}/\mu\text{s}$

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance Junction to Case	TO-220, TO-263	-	-	1.75	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-220, TO-263	-	-	62	$^\circ\text{C}/\text{W}$

Typical Performance Curves

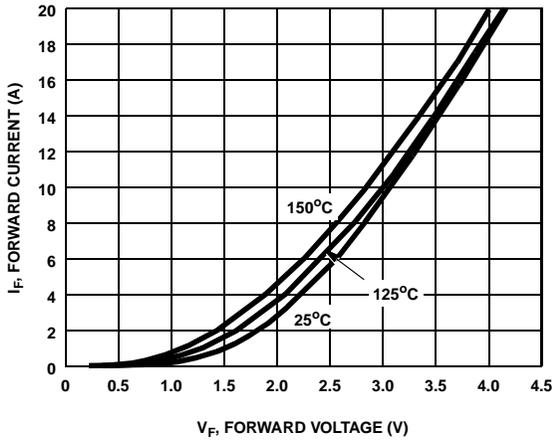


Figure 1. Forward Current vs Forward Voltage

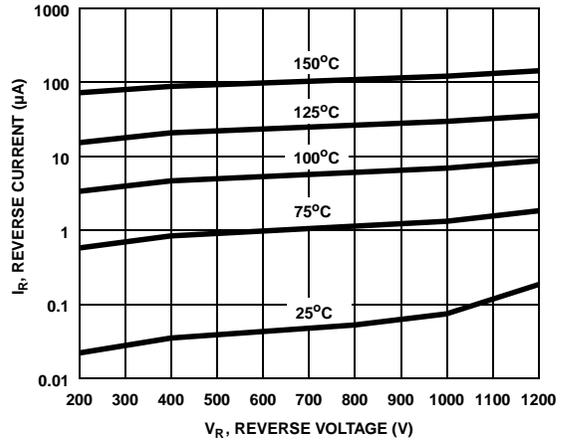


Figure 2. Reverse Current vs Reverse Voltage

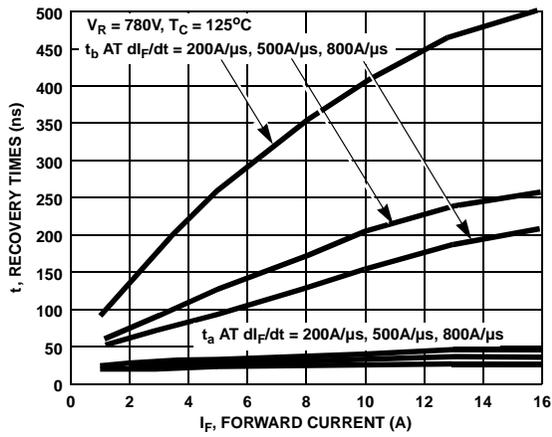


Figure 3. t_a and t_b Curves vs Forward Current

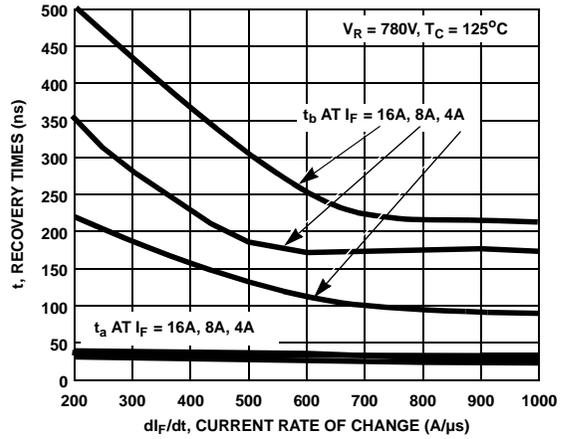


Figure 4. t_a and t_b Curves vs di_F/dt

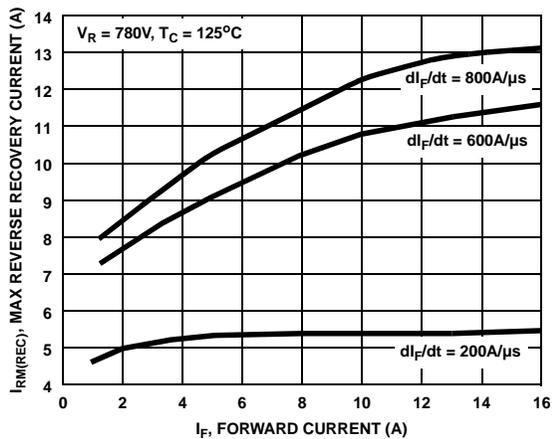


Figure 5. Maximum Reverse Recovery Current vs Forward Current

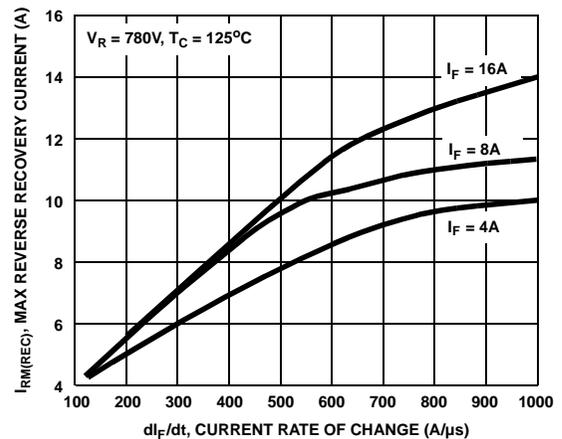


Figure 6. Maximum Reverse Recovery Current vs di_F/dt

Typical Performance Curves (Continued)

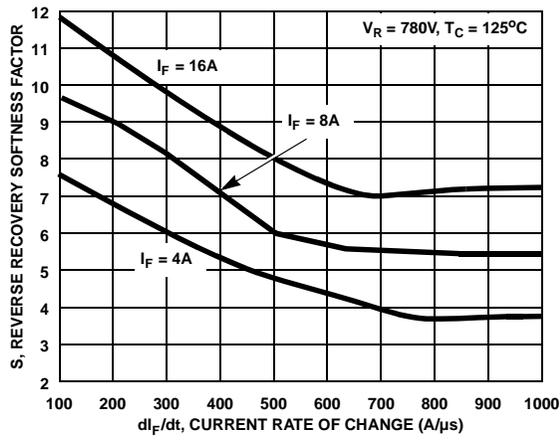


Figure 7. Reverse Recovery Softness Factor vs di_F/dt

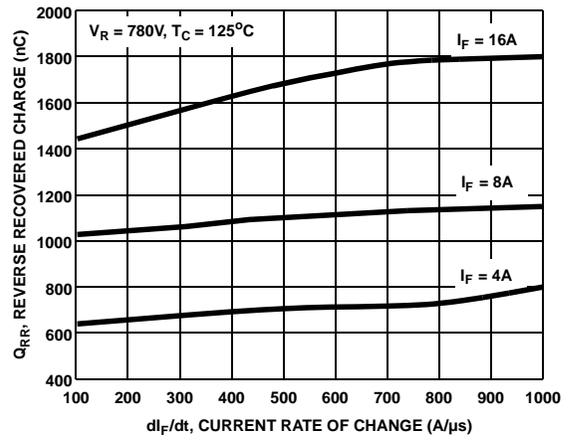


Figure 8. Reverse Recovered Charge vs di_F/dt

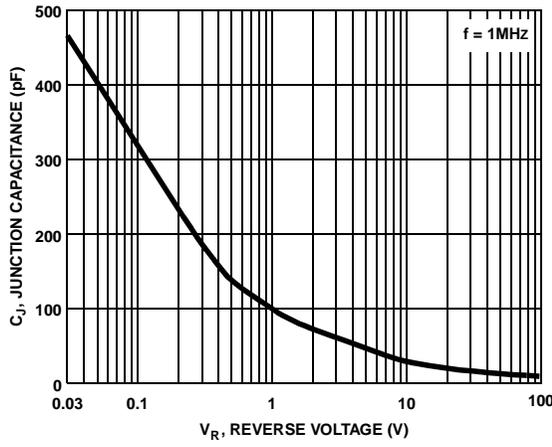


Figure 9. Junction Capacitance vs Reverse Voltage

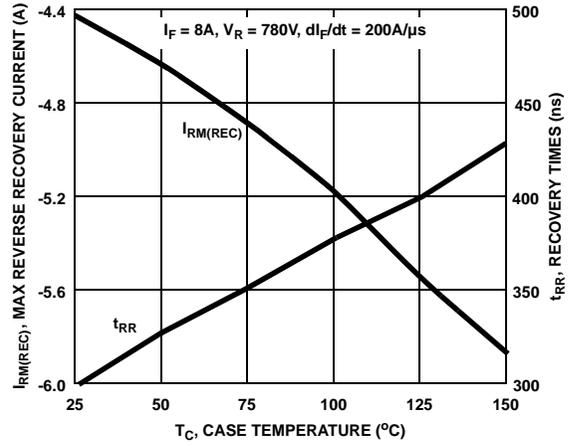


Figure 10. Reverse Recovery Current and Times vs Case Temperature

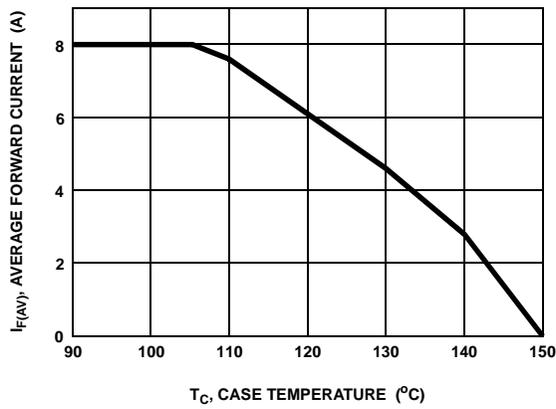


Figure 11. DC Current Derating Curve

Typical Performance Curves (Continued)

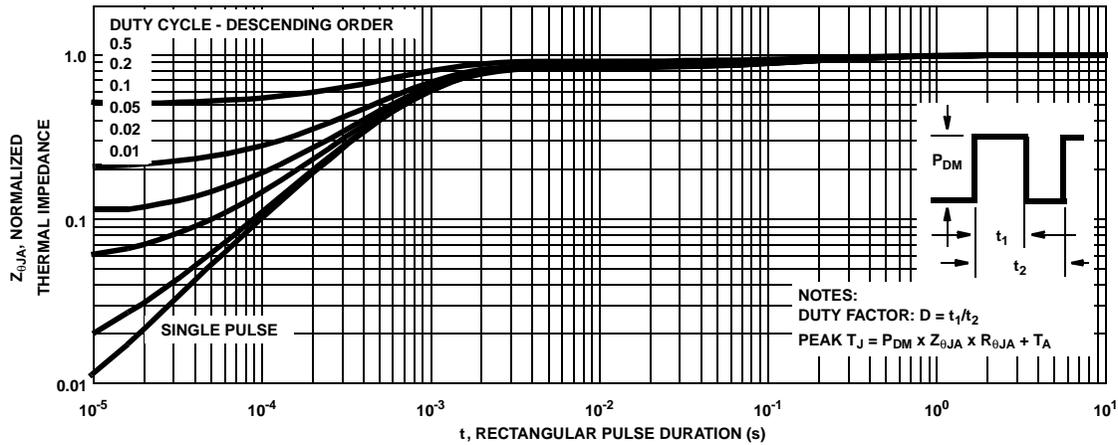


Figure 12. Normalized Maximum Transient Thermal Impedance

Test Circuit and Waveforms

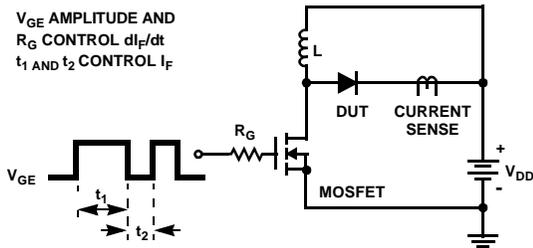


Figure 13. I_{trr} Test Circuit

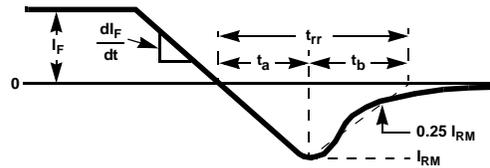


Figure 14. t_{rr} Waveforms and Definitions

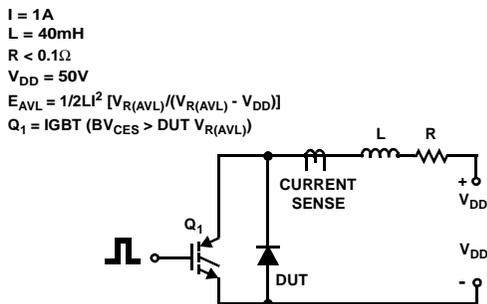


Figure 15. Avalanche Energy Test Circuit

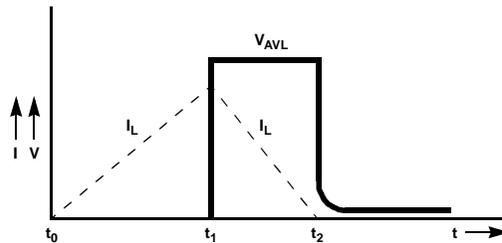


Figure 16. Avalanche Current and Voltage Waveforms



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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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Rev. I64

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