

**4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY**
**Product Summary**

<b>V<sub>BR(MIN)</sub></b>	<b>I<sub>PP(MAX)</sub></b>	<b>C<sub>T(TYP)</sub></b>
3.8V	25A	3.8pF

**Description**

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

**Applications**

- Cellular Handsets
- Portable Electronics
- Computers and Peripheral

**Features**

- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- 4 Channels of ESD Protection
- Low Channel Input Capacitance
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

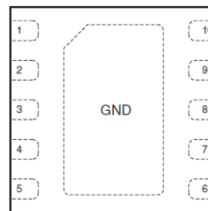
**Mechanical Data**

- Case: U-DFN2626-10
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 <sup>(e4)</sup>
- Weight: 0.01 grams (Approximate)

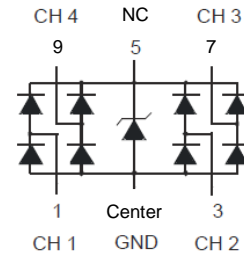
Pin #	Description
1, 3, 7, 9	I/Os
2, 4, 6, 8, 10	No Connect
5	No Connect (Do not connect to a DC supply)
Center Tab	Ground

Pin Description

U-DFN2626-10



Top View

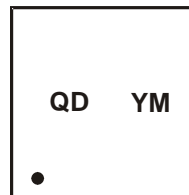


Device Schematic

**Ordering Information** (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
D3V3P4U10LP26-7	Standard	QD	7	8	3,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

**Marking Information**


QD = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: D = 2016)  
 M = Month (ex: 9 = September)

## Date Code Key

Year	2014	2015	2016	2017	2018	2019	2020
Code	B	C	D	E	F	G	H

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current	I <sub>PP</sub>	25	A	8/20μs (Note 7)
ESD Protection – Contact Discharge	V <sub>ESD_Contact</sub>	±30	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V <sub>ESD_Air</sub>	±30	kV	Standard IEC 61000-4-2

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	500	mW
Thermal Resistance, Junction to Ambient T <sub>A</sub> = +25°C	R <sub>θJA</sub>	250	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Standoff Voltage	V <sub>RWM</sub>	—	—	3.3	V	—
Channel Leakage Current (Note 6)	I <sub>R</sub>	—	—	1000	nA	V <sub>R</sub> = 3.3V, Any I/O to GND
Reverse breakdown voltage	V <sub>BR</sub>	3.8	—	6.5	V	I <sub>R</sub> = 1mA, from pin 5 to pin 2
Clamping Voltage, Positive Transients (Note 7)	V <sub>C</sub>	—	—	6.7	V	I <sub>PP</sub> = 1A, t <sub>p</sub> = 8/20μs
		—	—	8.5	V	I <sub>PP</sub> = 10A, t <sub>p</sub> = 8/20μs
		—	—	12	V	I <sub>PP</sub> = 25A, t <sub>p</sub> = 8/20μs
Channel Input Capacitance (Note 8)	C <sub>T</sub>	—	3.8	5	pF	V <sub>R</sub> = 0V, f = 1MHz, Any I/O to GND
Dynamic Resistance	R <sub>DYN</sub>	—	0.3	—	Ω	I <sub>PP</sub> = 1A, t <sub>p</sub> = 8/20μs

- Notes:
- Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at <http://www.diodes.com>.
  - Short duration pulse test used to minimize self-heating effect.
  - Clamping voltage value is based on an 8x20μs peak pulse current (I<sub>PP</sub>) waveform.
  - Measured from any I/O to GND.
  - For information on the impact of Diodes' USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL: [http://www.diodes.com/destdools/appnote\\_dnote.html](http://www.diodes.com/destdools/appnote_dnote.html).

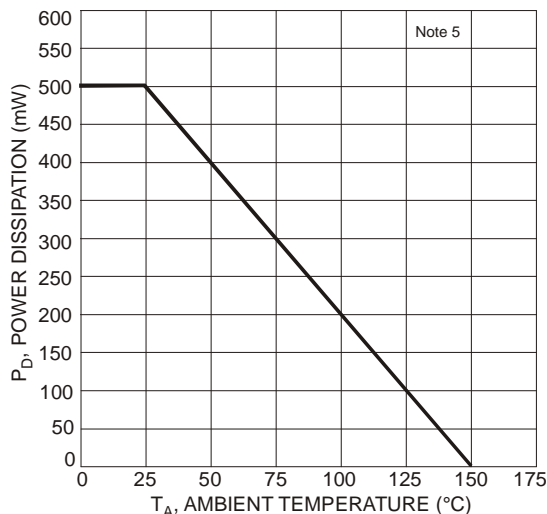


Figure 1 Power Derating Curve

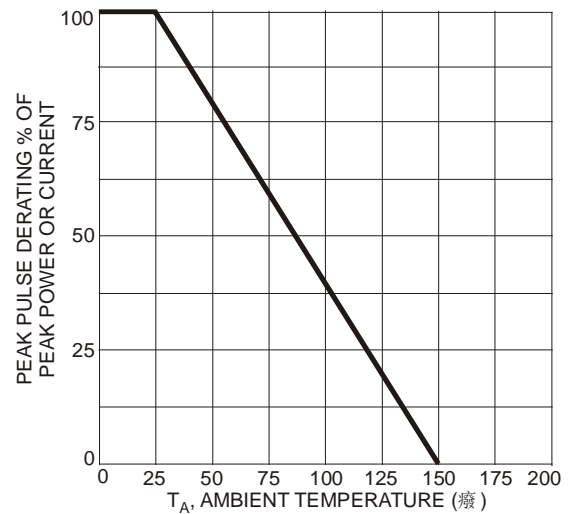


Figure 2 Pulse Derating (°C)

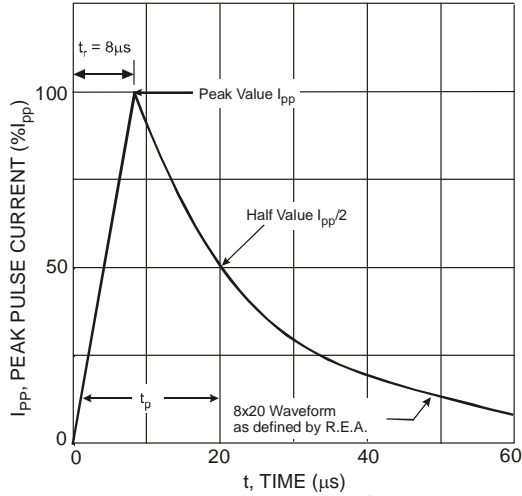


Figure 3 Pulse Waveform

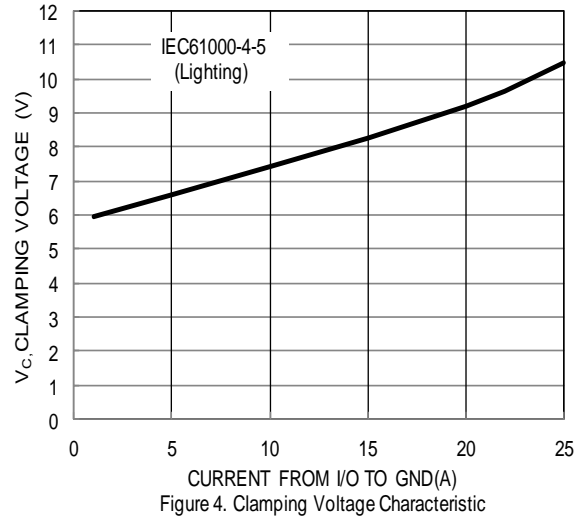
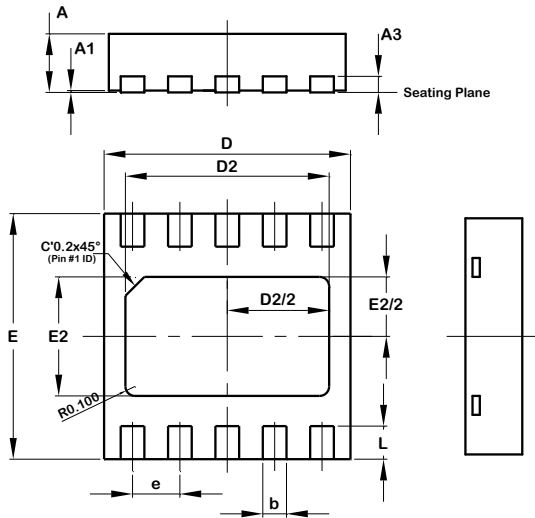


Figure 4. Clamping Voltage Characteristic

**Package Outline Dimensions**

Please see AP02001 at [http://www.diodes.com/\\_files/datasheets/ap02001.pdf](http://www.diodes.com/_files/datasheets/ap02001.pdf) for the latest version.

**U-DFN2626-10**

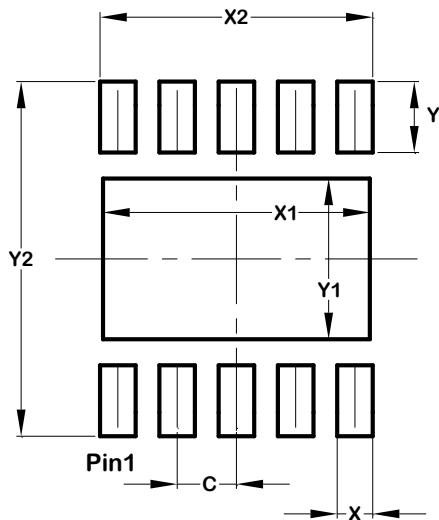


U-DFN2626-10			
Dim	Min	Max	Typ
A	0.57	0.63	0.60
A1	0	0.05	0.03
A3	-	-	0.15
b	0.20	0.30	0.25
D	2.55	2.675	2.60
D2	2.05	2.25	2.15
E	2.55	2.675	2.60
E2	1.16	1.36	1.26
e	0.50 BSC		
L	0.30	0.40	0.35
All Dimensions in mm			

**Suggested Pad Layout**

Please see AP02001 at [http://www.diodes.com/\\_files/datasheets/ap02001.pdf](http://www.diodes.com/_files/datasheets/ap02001.pdf) for the latest version.

**U-DFN2626-10**



Dimensions	Value (in mm)
C	0.500
X	0.300
X1	2.250
X2	2.300
Y	0.600
Y1	1.360
Y2	3.000

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