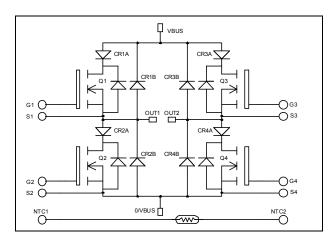


# Full bridge Series & parallel diodes MOSFET Power Module

$$\begin{split} V_{DSS} &= 1000 V \\ R_{DSon} &= 450 m\Omega \text{ typ } @ \text{ Tj} = 25^{\circ} \text{C} \\ I_D &= 18 \text{A} @ \text{ Tc} = 25^{\circ} \text{C} \end{split}$$



S4 **A** 

S2 🛭

O/VBUS

OUT2

OUTI

### Application

- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

#### **Features**

- Power MOS 7<sup>®</sup> MOSFETs
  - Low R<sub>DSon</sub>
  - Low input and Miller capacitance
  - Low gate charge
  - Avalanche energy rated
  - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
  - Lead frames for power connections
- Internal thermistor for temperature monitoring
- High level of integration



- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

### All ratings @ $T_i = 25^{\circ}C$ unless otherwise specified

#### Absolute maximum ratings

**Ø** G3

**fi** S3

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		1000	V
Ţ	Continuous Drain Current	$T_c = 25^{\circ}C$	18	
$I_{D}$		$T_c = 80^{\circ}C$	14	A
$I_{DM}$	Pulsed Drain current		72	
$V_{GS}$	Gate - Source Voltage		±30	V
R <sub>DSon</sub>	Drain - Source ON Resistance		540	mΩ
$P_D$	Maximum Power Dissipation	$T_c = 25$ °C	357	W
$I_{AR}$	Avalanche current (repetitive and non repetitive)		18	Α
$E_{AR}$	Repetitive Avalanche Energy		50	
$E_{AS}$	Single Pulse Avalanche Energy		2500	mJ

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 1000V$ $T_j =$	25°C			100	4
		$V_{GS} = 0V, V_{DS} = 800V$ $T_j =$	125°C			500	μΑ
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 9A$			450	540	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 2.5 \text{mA}$		3		5	V
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$				±100	nA

**Dynamic Characteristics** 

·	Characteristic	Test Conditions	Min	Тур	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$		4350		
$C_{oss}$	Output Capacitance	$V_{\rm DS} = 25V$		715		pF
$C_{rss}$	Reverse Transfer Capacitance	f=1MHz		120		
$Q_{g}$	Total gate Charge	$V_{GS} = 10V$		154		
$Q_{gs}$	Gate – Source Charge	$V_{Bus} = 500V$		26		nC
$Q_{gd}$	Gate – Drain Charge	$I_D = 18A$		97		
$T_{d(on)}$	Turn-on Delay Time	Inductive switching @ 125°C $V_{GS} = 15V$ $V_{Bus} = 667V$ $I_D = 18A$		10		
$T_{r}$	Rise Time			12		ns
$T_{d(off)}$	Turn-off Delay Time			121		
$T_{\mathrm{f}}$	Fall Time	$R_G = 5\Omega$		35		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		639		1
$E_{\text{off}}$	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 667V$ $I_D = 18A, R_G = 5\Omega$		380		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		1046		1
$E_{\text{off}}$	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 667V$ $I_D = 18A, R_G = 5\Omega$		451		μJ
$R_{thJC}$	Junction to Case Thermal Resistance				0.35	°C/W

Series diode ratings and characteristics

Symbol	Characteristic Test Conditions		Min	Typ	Max	Unit		
$V_{RRM}$	Maximum Peak Repetitive Reverse Vol	age		1000			V	
$I_{RM}$	Maximum Reverse Leakage Current	$V_R = 1000V$				250	μΑ	
$I_F$	DC Forward Current		$T_c = 65^{\circ}C$		30		Α	
		$I_F = 30A$			1.9	2.3		
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 60A$			2.2		V	
		$I_F = 30A$	$T_{j} = 125^{\circ}C$		1.7			
+	Daviana Dagayani Tima	$T_j =$	$T_j = 25$ °C		290		ne	
$t_{rr}$	Reverse Recovery Time	$I_F = 30A$ $V_R = 667V$	$T_{j} = 125^{\circ}C$		390		ns	
Q <sub>rr</sub>	Reverse Recovery Charge	$di/dt = 200 A/\mu$	$di/dt = 200A/\mu s$	$T_j = 25^{\circ}C$		670		nC
			$T_{j} = 125^{\circ}C$		2350		110	
$R_{thJC}$	Junction to Case Thermal Resistance					1.2	°C/W	



Parallel diode ratings and characteristics

Symbol	Characteristic Test Conditions		Min	Typ	Max	Unit		
$V_{RRM}$	Maximum Peak Repetitive Reverse Vol	tage		1000			V	
$I_{RM}$	Maximum Reverse Leakage Current	$V_R = 1000V$				250	μΑ	
$I_{F}$	DC Forward Current		$T_c = 65^{\circ}C$		30		A	
		$I_{\rm F} = 30 A$			1.9	2.3		
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 60A$			2.2		V	
		$I_F = 30A$	$T_{j} = 125^{\circ}C$		1.7			
+	Davarga Dagayary Time		$T_j = 25$ °C		290		ne	
$t_{rr}$	Reverse Recovery Time	$I_F = 30A$ $V_R = 667V$	$T_j = 125^{\circ}C$		390		ns	
$Q_{rr}$	Reverse Recovery Charge	di/dt = 200 A/us	$di/dt = 200A/\mu s$	$T_j = 25$ °C		670		пC
≺rr			$T_{j} = 125^{\circ}C$		2350		110	
$R_{thJC}$	Junction to Case Thermal Resistance					1.2	°C/W	

Thermal and package characteristics

Symbol	Characteristic			Min	Max	Unit		
$V_{ISOL}$	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000		V		
$T_{J}$	Operating junction temperature range			-40	150			
$T_{JOP}$	Recommended junction temperature under switching conditions			-40	T <sub>J</sub> max -25	°C		
$T_{STG}$	Storage Temperature Range			-40	125			
$T_{\rm C}$	Operating Case Temperature			-40	100			
Torque	Mounting torque	To Heatsink	M5	2.5	4.7	N.m		
Wt	Package Weight				160	g		

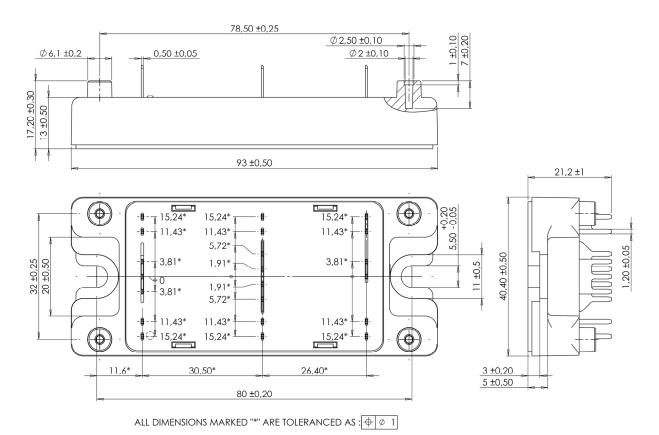
Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic	,	Min	Тур	Max	Unit
R <sub>25</sub>	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
B <sub>25/85</sub>	$T_{25} = 298.15 \text{ K}$	K		3952		K
$\Delta B/B$		T <sub>C</sub> =100°C		4		%

$$R_T = \frac{R_{25}}{\exp \left[ B_{25/85} \left( \frac{1}{T_{..}} - \frac{1}{T} \right) \right]}$$
 T: Thermistor temperature R<sub>T</sub>: Thermistor value at T



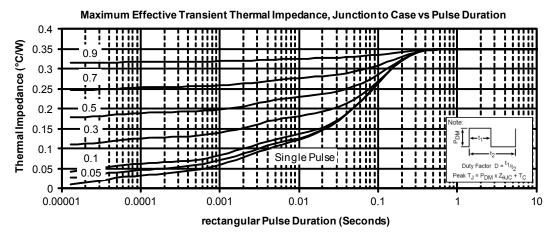
### SP4 Package outline (dimensions in mm)

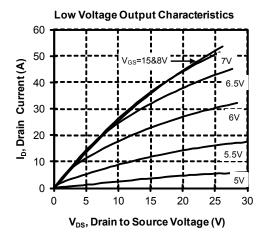


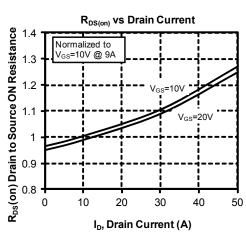
See application note APT0501 - Mounting Instructions for SP4 Power Modules on www.microsemi.com

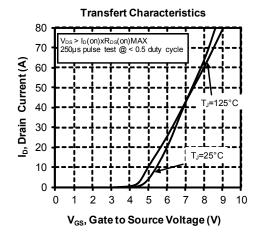


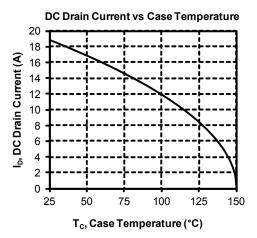
### **Typical Performance Curve**



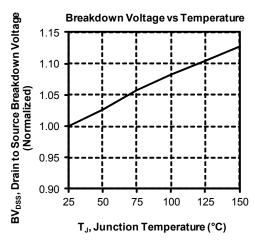


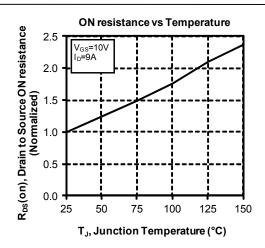


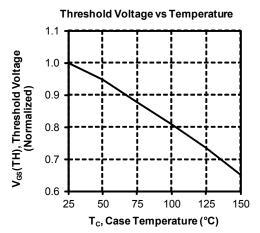


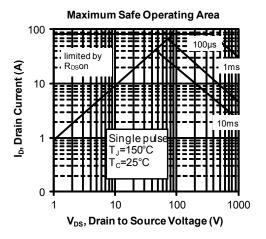


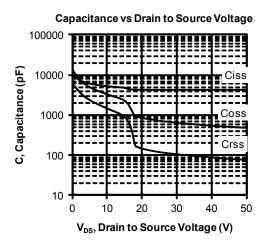


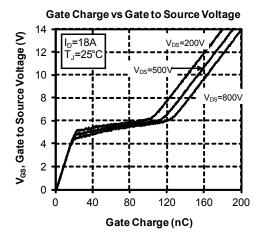




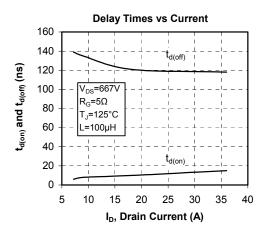


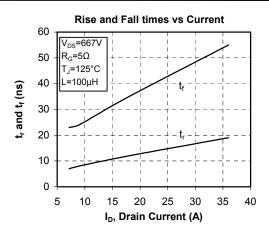


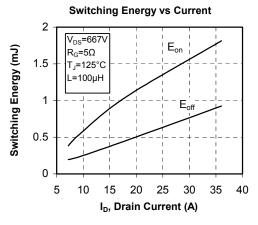


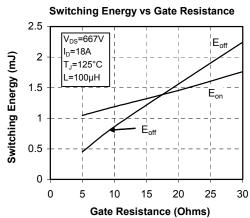


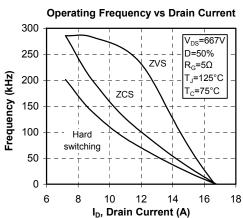


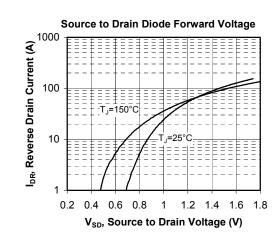












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