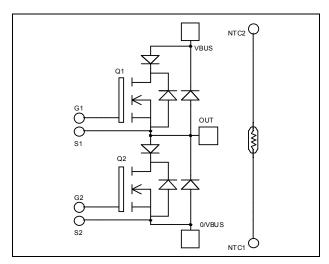


Phase leg Series & parallel diodes MOSFET Power Module

$$\begin{split} V_{DSS} &= 1000 V \\ R_{DSon} &= 230 m \Omega \ typ \ @ \ Tj = 25^{\circ} C \\ I_D &= 36 A \ @ \ Tc = 25^{\circ} C \end{split}$$



O/VBUS

S2

Application

- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

Features

- Power MOS 7[®] MOSFETs
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Avalanche energy rated
 - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
 - Symmetrical design
 - Lead frames for power connections
- Internal thermistor for temperature monitoring
- High level of integration



- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

All ratings (a) $T_i = 25$ °C unless otherwise specified

Absolute maximum ratings

VBUS

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		1000	V
Ţ	In Continuous Drain Current	$T_c = 25$ °C	36	
1_{D}		$T_c = 80$ °C	27	A
I_{DM}	Pulsed Drain current		144	
V_{GS}	Gate - Source Voltage		±30	V
R _{DSon}	Drain - Source ON Resistance		270	mΩ
P_D	Maximum Power Dissipation	$T_c = 25^{\circ}C$	694	W
I_{AR}	Avalanche current (repetitive and non repetitive)		18	A
E _{AR}	Repetitive Avalanche Energy		50	ma I
E_{AS}	Single Pulse Avalanche Energy		2500	mJ

OUT

OUT

NTC2

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
I_{DSS} Z	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 1000V$	$T_j = 25$ °C			200	4
		$V_{GS} = 0V, V_{DS} = 800V$	$T_j = 125$ °C			1000	μΑ
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 18A$			230	270	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 5 \text{mA}$		3		5	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	1			±150	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$		8700		
C_{oss}	Output Capacitance	$V_{\rm DS} = 25V$		1430		pF
C_{rss}	Reverse Transfer Capacitance	f = 1MHz		240		
Q_{g}	Total gate Charge	$V_{GS} = 10V$		308		
Q_{gs}	Gate – Source Charge	$V_{Bus} = 500V$		52		nC
Q_{gd}	Gate – Drain Charge	$I_D = 36A$		194		
$T_{d(on)}$	Turn-on Delay Time	Inductive switching @ 125°C		10		
$T_{\rm r}$	Rise Time	$V_{GS} = 15V$ $V_{Bus} = 667V$ $I_D = 36A$		12		ns
$T_{d(off)}$	Turn-off Delay Time			121		
T_{f}	Fall Time	$R_G = 2.5\Omega$		35		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		1278		т.
E_{off}	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 667V$ $I_D = 36A, R_G = 2.5\Omega$		760		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		2092		_
E _{off}	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 667V$ $I_D = 36A, R_G = 2.5\Omega$		902		μJ
R_{thJC}	Junction to Case Thermal Resistance				0.18	°C/W

Series diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit		
V_{RRM}	Maximum Peak Repetitive Reverse Vol	tage		1000			V		
I_{RM}	Maximum Reverse Leakage Current	$V_R = 1000V$				500	μA		
I_F	DC Forward Current		$T_c = 65^{\circ}C$		90		A		
		$I_F = 90A$			1.9	2.3			
V_{F}	Diode Forward Voltage	$I_F = 180A$			2.2		V		
		$I_F = 90A$	$T_j = 125$ °C		1.7				
+	Reverse Recovery Time $I_F = 90A$	$T_j = 25$ °C		290		ng			
t _{rr}		$I_F = 90A$ $V_R = 667V$	$T_{j} = 125^{\circ}C$		390		ns		
Q _{rr}	Reverse Recovery Charge	di/dt =	di/dt = 400 A/us	di/dt = 400 A/us	$T_j = 25^{\circ}C$		2010		nC
			$T_{j} = 125^{\circ}C$		7050		IIC		
R_{thJC}	Junction to Case Thermal Resistance				·	0.45	°C/W		



Parallel diode ratings and characteristics

Symbol	Characteristic	acteristic Test Conditions		Min	Typ	Max	Unit		
V_{RRM}	Maximum Peak Repetitive Reverse Vol	tage		1000			V		
I_{RM}	Maximum Reverse Leakage Current	V _R =1000V				150	μA		
I_F	DC Forward Current		$T_c = 80$ °C		80		A		
		$I_F = 80A$			2.5	3.5			
V_{F}	Diode Forward Voltage	$I_{\rm F} = 140A$			3.1		V		
		$I_F = 80A$	$T_j = 125$ °C		2				
+	Reverse Recovery Time		$T_j = 25$ °C		250		ne		
t_{rr}		$I_F = 80A$ $V_R = 667V$	$T_{j} = 125^{\circ}C$		315		ns		
Qrr	Reverse Recovery Charge	di/dt = 400 A/us		$di/dt = 400 \text{ A/us}$ $T_i = 25^{\circ}$	T 2500		830		C
		'	$T_{j} = 125^{\circ}C$		3300		nC		
R_{thJC}	Junction to Case Thermal Resistance					0.65	°C/W		

Thermal and package characteristics

Symbol	Characteristic			Min	Typ	Max	Unit
$V_{\rm ISOL}$	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T_{J}	Operating junction temperature range			-40		150	
T_{STG}	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature			-40		100	
Torque	Mounting torque	To Heatsink	M5	2.5		4.7	N.m
Wt	Package Weight					160	g

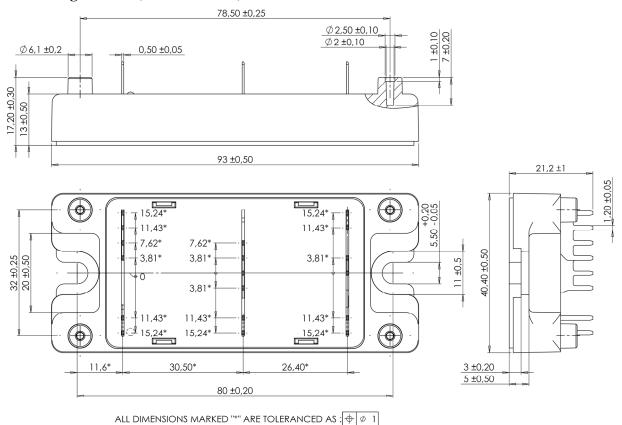
Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic	teristic		Typ	Max	Unit
R ₂₅	Resistance @ 25°C	5°C		50		kΩ
$\Delta R_{25}/R_{25}$				5		%
$B_{25/85}$	$T_{25} = 298.15 \text{ K}$			3952		K
ΔΒ/Β		T _C =100°C		4		%

$$R_T = \frac{R_{25}}{\exp \left[B_{25/85} \left(\frac{1}{T_{25}} - \frac{1}{T} \right) \right]}$$
 T: Thermistor temperature R_T: Thermistor value at T



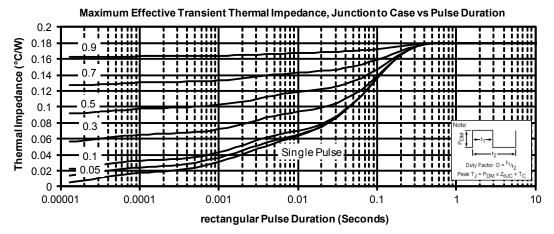
SP4 Package outline (dimensions in mm)

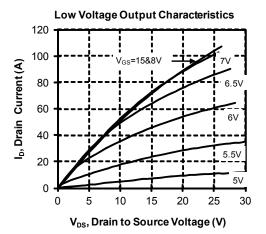


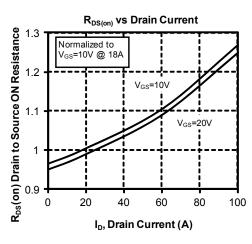
See application note APT0501 - Mounting Instructions for SP4 Power Modules on www.microsemi.com

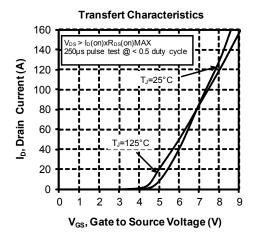


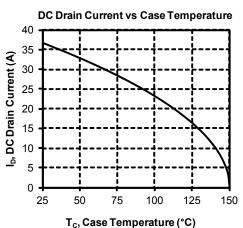
Typical Performance Curve



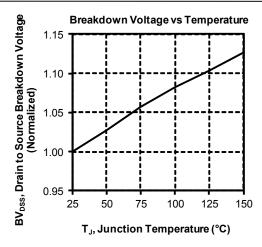


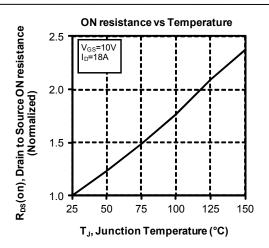


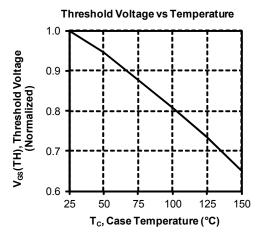


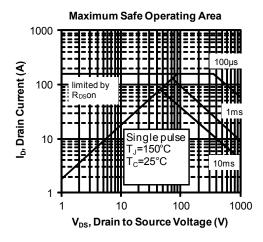


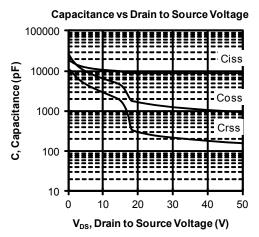


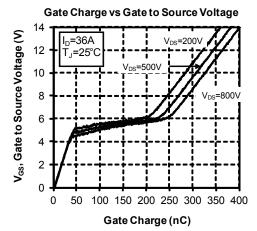




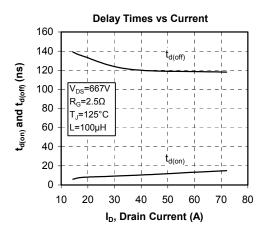


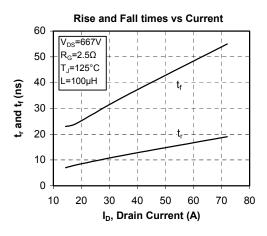


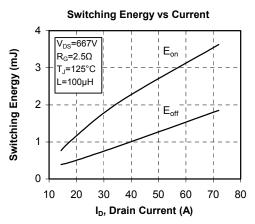


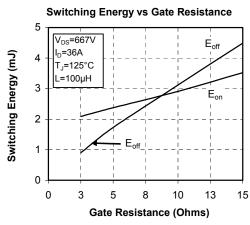


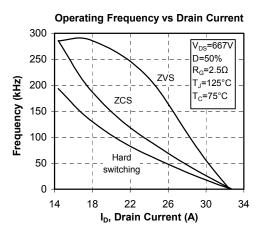


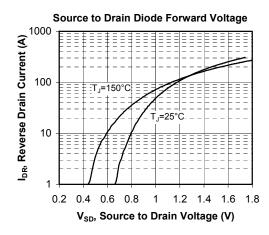












DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with life-support or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at http://www.microsemi.com/legal/tnc.asp

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.